

**B.L.D.E.A's**  
**S.B.Arts and K.C.P Science College, Vijayapur.**  
**Re-Acredited 'A' Level by NAAC**  
**M.Sc(CS) Programme**



**Seminar**  
**Sub: System Software**  
**Date: 28<sup>th</sup> August 2015**

**Conducted By:**  
**Prof (smt) R.D.Joshi**

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## NOTICE

All the students are hereby informed to attend the "System Software-Machine Architectures" seminars on different topics. The topics are as follows

SI No	USN	Student Name	Topics
1	CS151401	Akshata Bidanur	Ultra-Sparc Architecture
2	CS151403	Geeta Biradar	Pentium-Pro Architecture
3	CS151409	Sweta Biradar	VAX Architecure



**Co-ordinator**

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**IQAC, Co-ordinator**

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Vijayapur.



**Principal**

S.B.Arts & K.C.P.Science College,  
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## A Report on

A session on seminar was conducted on the 28th August 2015 at the Classroom, Department of M.Sc (CS). The seminar was attended by 8 students from first Semester of M.Sc (CS). The motive was to conduct a seminar and encourage the students and also, to observe their personality and skills, ability to present on the given subject and to guide them on how to take part in seminar.

The students given seminar on their individual topics. They discussed about different Machines architecture i.e Memory, Registers, Data Format, Instruction Format, Instruction Set, Input / Output operations. Finally students clarified their doubts regarding these architectures.



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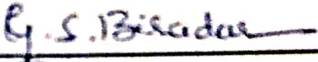

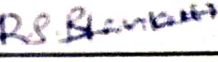
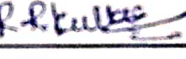
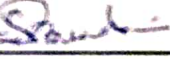


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**S.B.Arts & KCP Science College, Vijayapur.**  
**M.Sc(Computer Science)**  
**Student Seminar**

Sub: System Software -Machine Architecture

Date: 28/08/2015

Sl. No.	Reg .No	Name of the Students	Signature
1	CS151401	AKSHATA. S. BIDANUR	
2	CS151402	ANJANADEVILS.DHABADE	
3	CS151403	GEETA BIRADAR	
4	CS151404	PREETI . F. CHAVADI	
5	CS151405	RAJANI . S . BHAVIKATTI	
6	CS151406	RASHMI.R.KULKARNI	
7	CS151407	SANTOSH . S . BIRADAR	
8	CS151408	SUTAR KASAPPA BALWANT	
9	CS151409	SWETA BIRADAR	



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