



Department of Electrical and Electronics Engineering

(Accredited by NBA, New Delhi)

Organized

VGST Sponsored One Week Faculty Development Programme "Design & Implementation of Hardware Accelerators and Protocols on FPGA"



This is to certify that Mr./Ms Ashok	Basanagoudo	a Patil.	
of SBArts & KCP SC COL	lege. Vijayapul	has particip	ated in VGST Sponsored
One Week Faculty Development Programme on " Des	ign & Implementation of Ha	rdware Accelerators and I	Protocols on FPGA "
organized by the Department of Electrical & Electronics	Engineering, B.L.D.E.A's V. P. Dr. P. C	3. Halakatti College of Engineering	and Technology, Vijayapura
from 13th to 17th December 2018.	$M \supset$	1	-5
Programme Co-Ordinator / Convener Dr. Smt S. R. Savanur	S.B.Arta & K.C.P.Science College,	Principal,	Principal B.L.D.E.A's V.P.Dr. R.C. Halakam College of Energ. & Tech Ratron 3e163
Dr. Smt S. R. Savanur	Vijayapur,	VIIAVADID College	Dr. V. P. Huggi

Prof. & Head, Dept. of E&E

Principal