

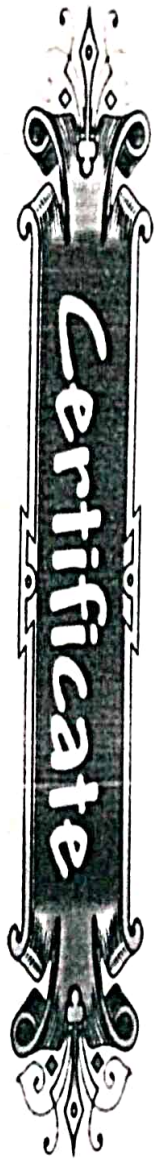


B.L.D.E Association's
 V.P.Dr. P.G.Halakatti College of Engineering & Technology,
 Vijayapura-586103

Department of Electrical and Electronics Engineering

(Accredited by NBA, New Delhi)
 Organized

VGST Sponsored One Week Faculty Development Programme
 on
 "Design & Implementation of Hardware Accelerators and Protocols on FPGA"



This is to certify that Mr/Ms Dr. Girija M Nimbal

of S.B.Arts & K.C.P.Sc College, Vijayapur has participated in VGST Sponsored

One Week Faculty Development Programme on "Design & Implementation of Hardware Accelerators and Protocols on FPGA

organized by the Department of Electrical & Electronics Engineering, B.L.D.E.A's V. P. Dr. P. G. Halakatti College of Engineering and Technology, Vijayapur from 13th to 17th December 2018.

S. Savanur

Programme Co-Ordinator / Convener
 Dr. Smt S. R. Savanur
 Prof. & Head, Dept. of E&E

[Signature]

IQAC, Co-ordinator
 S.B.Arts & K.C.P.Science College,
 Vijayapur.

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Principal,
 S.B. Arts and KCP Science College
 VIJAYAPUR

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